

WHAT IS CLAIMED IS:

1. An electroluminescent display comprising:
at least one scan line;
a plurality of data lines;
5 a plurality of light-emitting devices; and
a plurality of electrical addressing devices respectively coupling each of the
light-emitting devices to one data line and the at least one scan line;
wherein the light-emitting devices include display areas arranged according to a
nonuniform distribution along the at least one scan line.
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2. The electroluminescent display of claim 1, wherein the display areas are
offset from one another in a direction approximately perpendicular to the at least one
scan line.
- 15 3. The electroluminescent display of claim 1, wherein the light-emitting
devices coupled with the at least one scan line include at least a first display area and a
second display area having differently leveled portions.
4. The electroluminescent display of claim 1, wherein the at least one scan
20 line forms a crenelated profile.
5. The electroluminescent display of claim 4, wherein the display areas are
alternately placed at two sides of the at least one scan line formed in a crenelated
profile.

6. The electroluminescent display of claim 1, wherein the light-emitting devices include organic light-emitting diodes.

5 7. The electroluminescent display of claim 1, wherein one or more of the electrical addressing devices includes the coupling of a switch thin film transistor and a driver thin film transistor.

8. The electroluminescent display of claim 7, wherein the switch thin film transistor is coupled to the at least one scan line and one data line to respectively receive addressing and image signals, and the driver thin film transistor is coupled to the switch thin film transistor to deliver an electric current to one light-emitting device.

9. A liquid crystal display comprising:
15 at least one scan line;
a plurality of data lines;
a plurality of display electrodes; and
a plurality of electrical addressing devices respectively coupling each display electrode to one data line and the at least one scan line;
20 wherein the display electrodes are arranged according to a nonuniform distribution along the at least one scan line.

10. The liquid crystal display of claim 9, wherein the display electrodes are offset from one another in a direction approximately perpendicular to the at least one scan line.

5 11. The liquid crystal display of claim 9, wherein the display electrodes coupled with the at least one scan line include at least a first display area and a second display area having differently leveled portions.

12. The liquid crystal display of claim 9, wherein the at least one scan line
10 forms a crenelated profile.

13. The electroluminescent display of claim 12, wherein the display electrodes are alternately placed at two sides of the at least one scan line formed in a crenelated profile.

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14. The liquid crystal display of claim 9, wherein one or more of the electrical addressing devices includes a thin film transistor having a gate terminal connected to the at least one scan line, a drain terminal connected to one data line, and a source terminal connected to one display electrode.

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15. A display pixel array comprising:
at least one scan line;
a plurality of data lines;
a plurality of pixels, each pixel including one or more color subpixels; and

a plurality of electrical addressing devices respectively coupling each color subpixel to one data line and the at least one scan line;

wherein the color subpixels include display areas arranged according to a nonuniform distribution along the at least one scan line.

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16. The pixel array of claim 15, wherein the display areas are offset from one another in a direction approximately perpendicular to the at least one scan line.

17. The pixel array of claim 15, wherein the subpixels coupled with the at
10 least one scan line include at least a first display area and a second display area having differently leveled portions.

18. The pixel array of claim 15, wherein the at least one scan line forms a crenelated profile.

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19. The pixel array of claim 18, wherein the display areas are alternately placed at two sides of the at least one scan line formed in a crenelated profile.

20. The pixel array of claim 15, wherein one or more color subpixels
20 respectively includes one display electrode coupled with one electrical addressing device.

21. The pixel array of claim 20, wherein one or more electrical addressing device respectively includes a thin film transistor having a gate terminal connected to

the at least one scan line, a drain terminal connected to one data line, and a source terminal connected to one display electrode.

22. The pixel array of claim 15, wherein one or more subpixels respectively
5 include one light-emitting device coupled to one electrical addressing device.

23. The pixel array of claim 22, wherein one or more electrical addressing
devices respectively includes the coupling of a switch thin film transistor and a driver
thin film transistor.

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24. The pixel array of claim 23, wherein the switch thin film transistor is
coupled to the at least one scan line and one data line to respectively receive addressing
and image signals, and the driver thin film transistor is coupled to the switch thin film
transistor to deliver an electric current to one light-emitting device.

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25. The pixel array of claim 15, wherein the pixels are arranged according to
a delta configuration.

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